

Fig. 1

FIG. 2 is a block diagram of a Switch Protocol Controller (SPC) 200. The SPC 200 includes a Receive Controller 201, a Decoder 202, a Header Processor 203, a Frame Buffer 204, an Arbitrator 206, and a Transmit Controller 205. The SPC 200 is connected to an Arbitration Bus 200 and a To/from Crosspoint Switch. The input of the SPC 200 is connected to the Receive Controller 201, and the output is connected to the Transmit Controller 205.

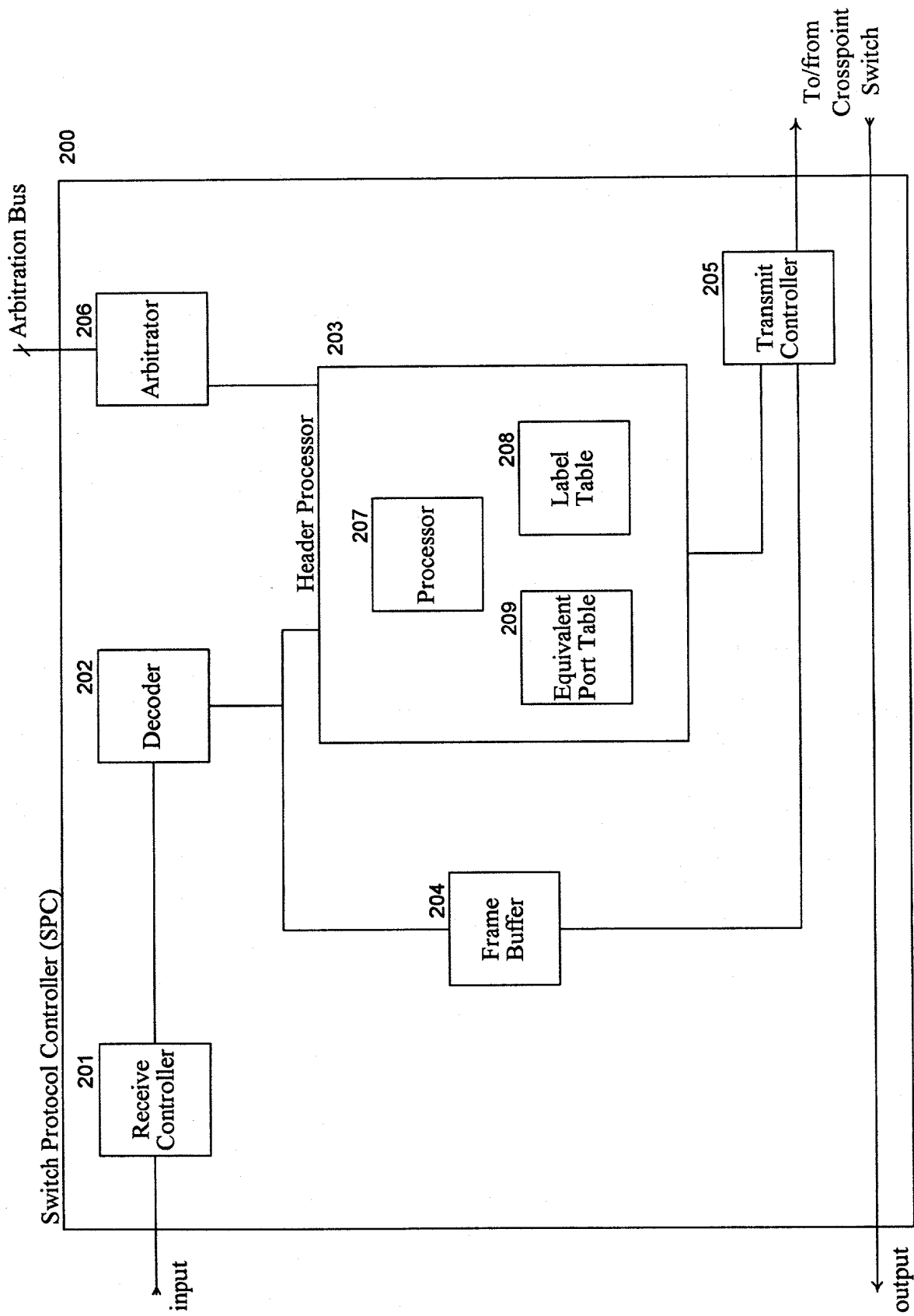


Fig. 2

Figure 1: Label Table. The diagram shows a table with columns for Port # (0, 1, 2, 3) and Virtual Address (0, 1, 2, 3). The table contains binary values (0, 1) and is labeled "Label Table".

| Port # | Virtual Address | 0 | 1 | 2 | 3 |
|--------|-----------------|---|---|---|---|
| 0 | 0 | 0 | 0 | 0 | 0 |
| 0 | 1 | 1 | 1 | 1 | 1 |
| 0 | 2 | 0 | 0 | 0 | 0 |
| 0 | 3 | 0 | 0 | 0 | 0 |
| 1 | 0 | 0 | 0 | 0 | 0 |
| 1 | 1 | 1 | 1 | 1 | 1 |
| 1 | 2 | 0 | 0 | 0 | 0 |
| 1 | 3 | 0 | 0 | 0 | 0 |
| 2 | 0 | 0 | 0 | 0 | 0 |
| 2 | 1 | 1 | 1 | 1 | 1 |
| 2 | 2 | 0 | 0 | 0 | 0 |
| 2 | 3 | 0 | 0 | 0 | 0 |
| 3 | 0 | 0 | 0 | 0 | 0 |
| 3 | 1 | 1 | 1 | 1 | 1 |
| 3 | 2 | 0 | 0 | 0 | 0 |
| 3 | 3 | 0 | 0 | 0 | 0 |

Fig. 3

0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000
 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000
 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000

Frame

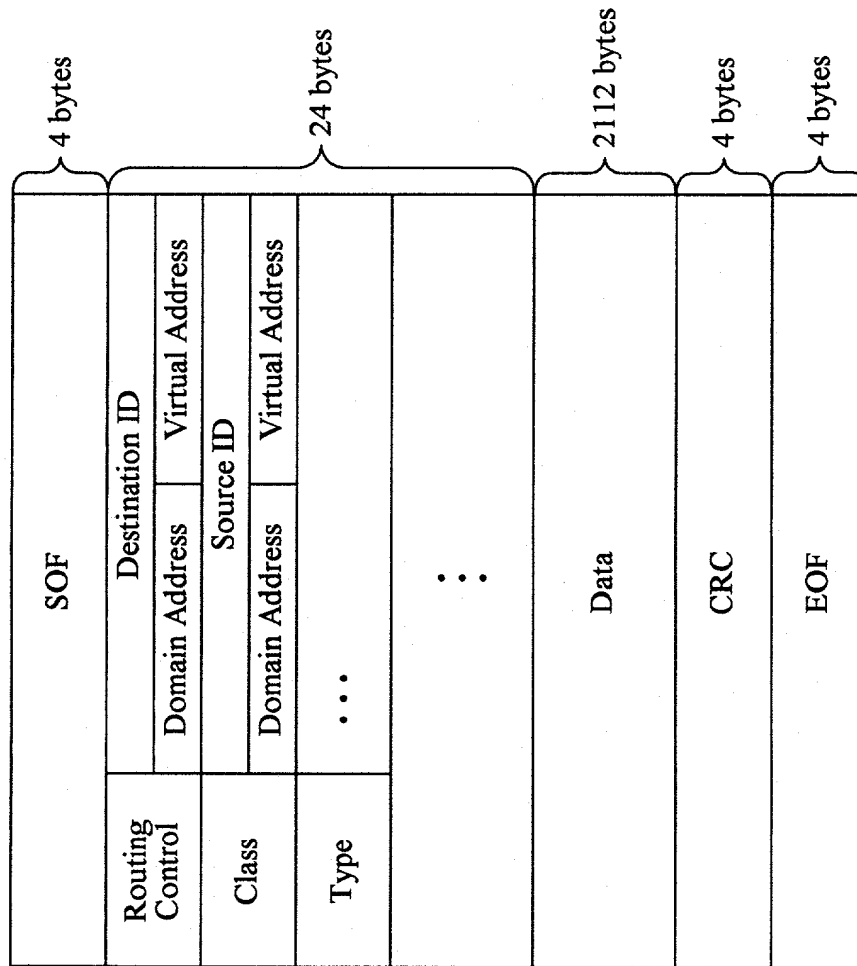


Fig. 4

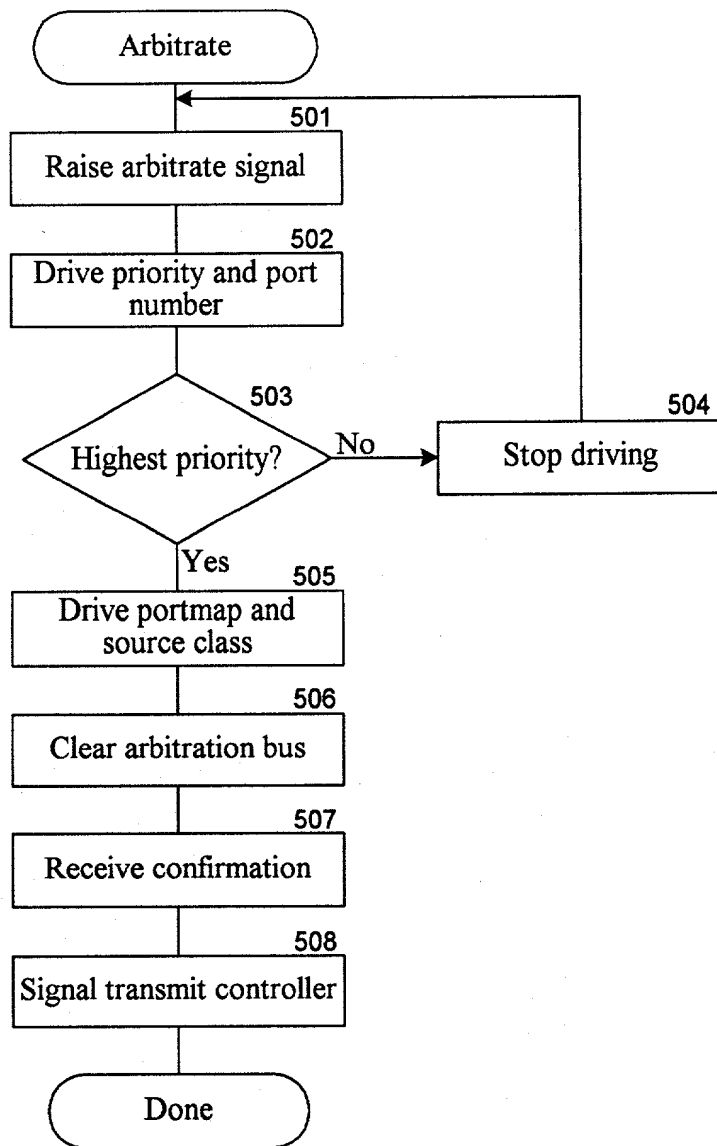


Fig. 5

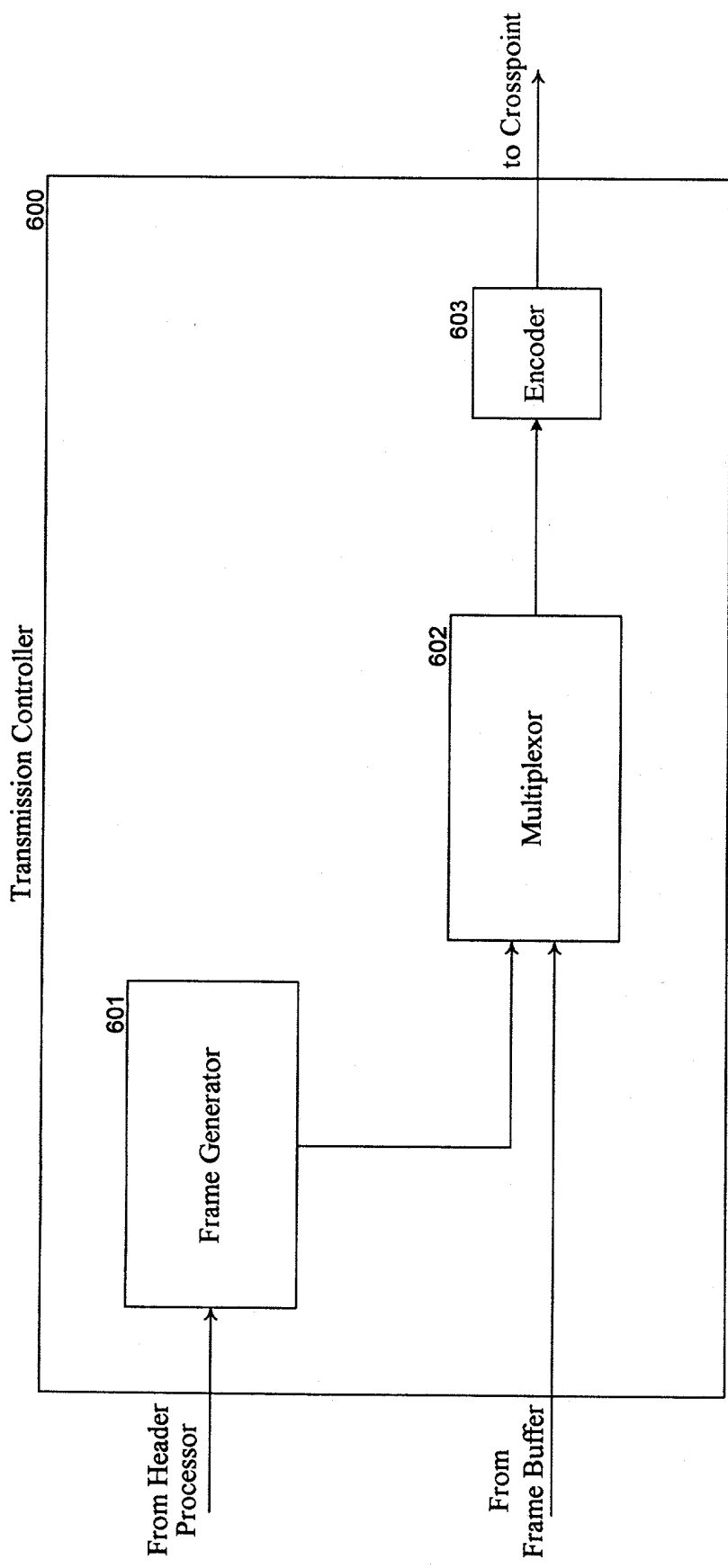


Fig. 6

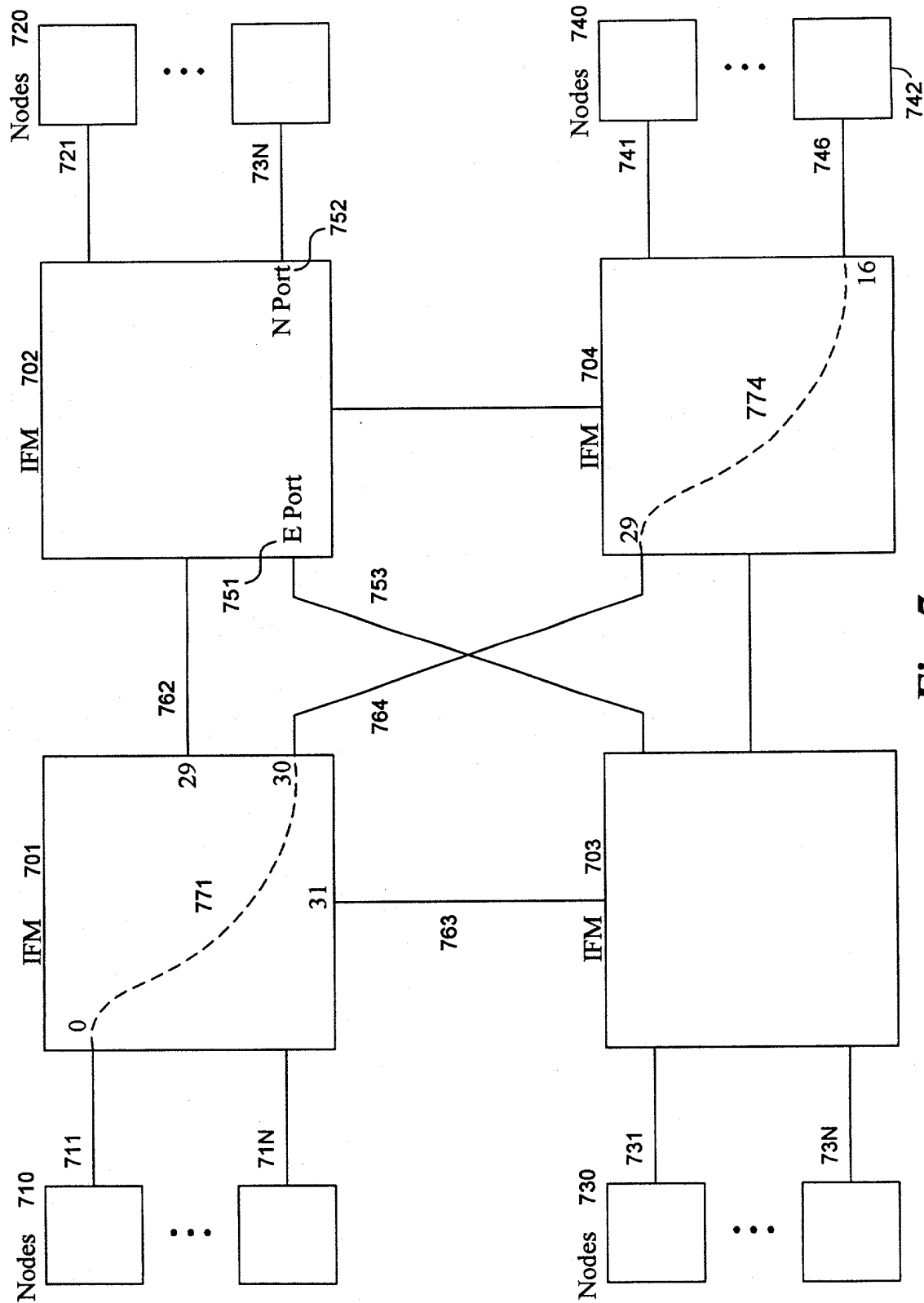


Fig. 7

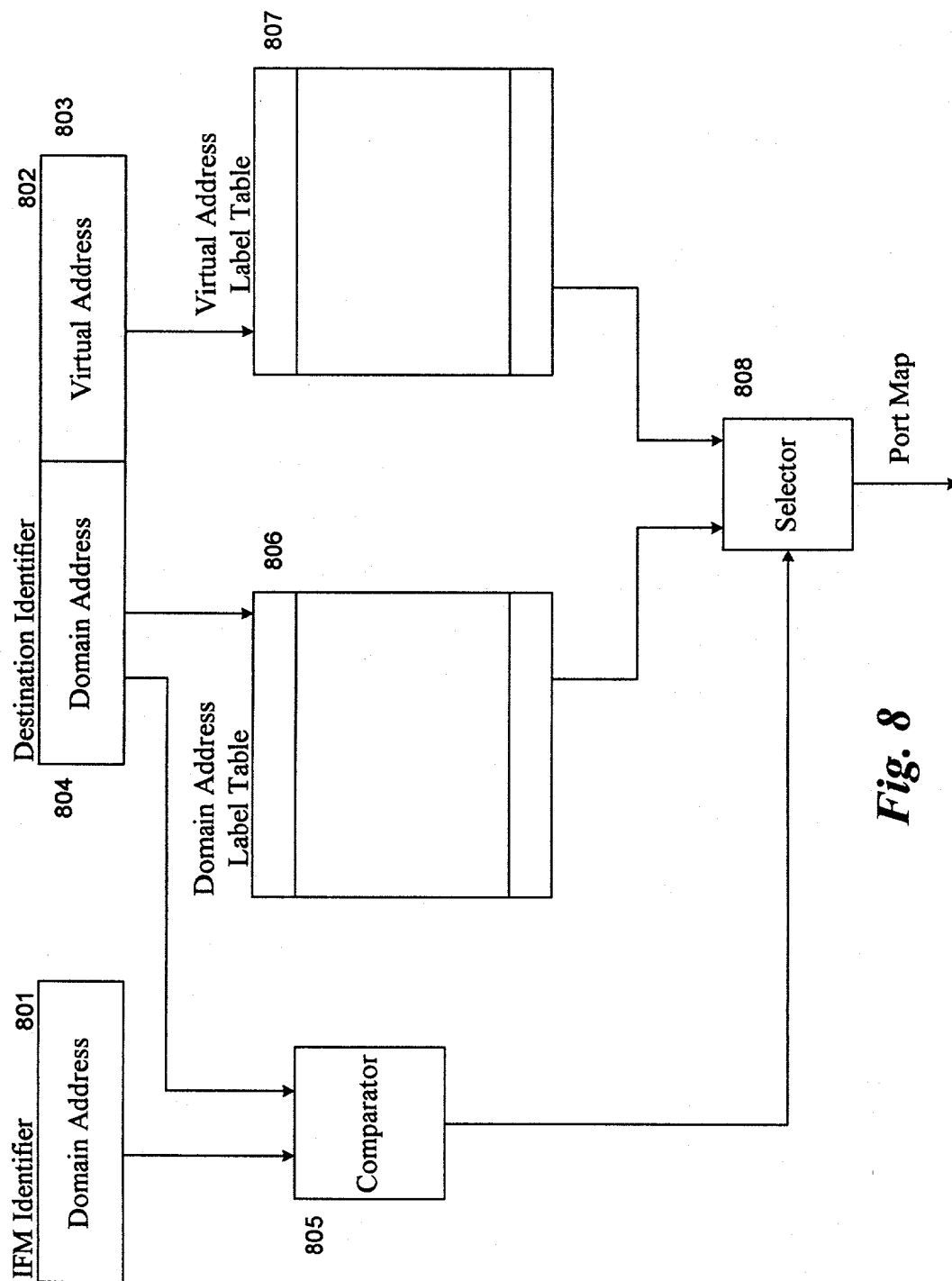


Fig. 8

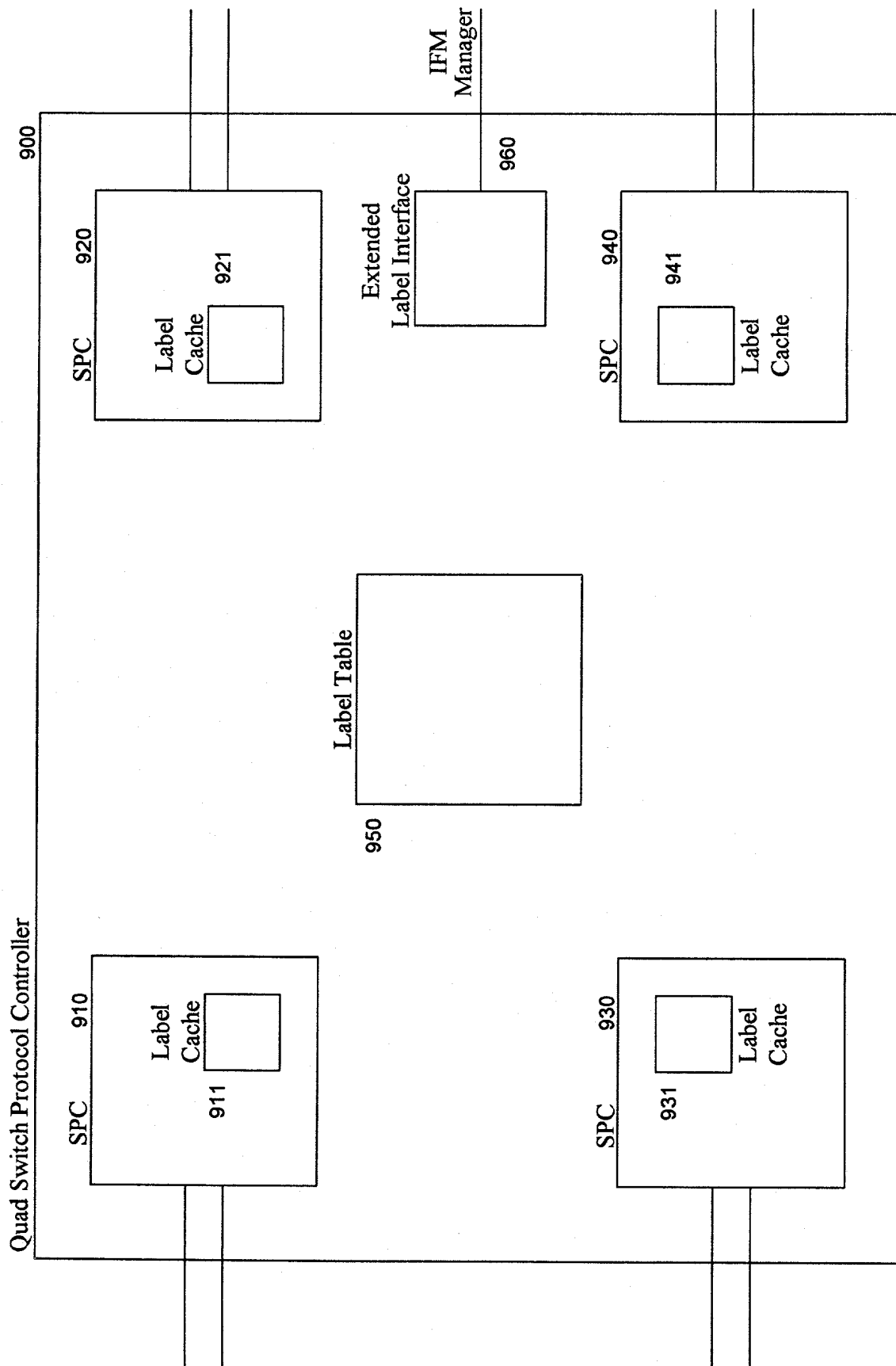


Fig. 9

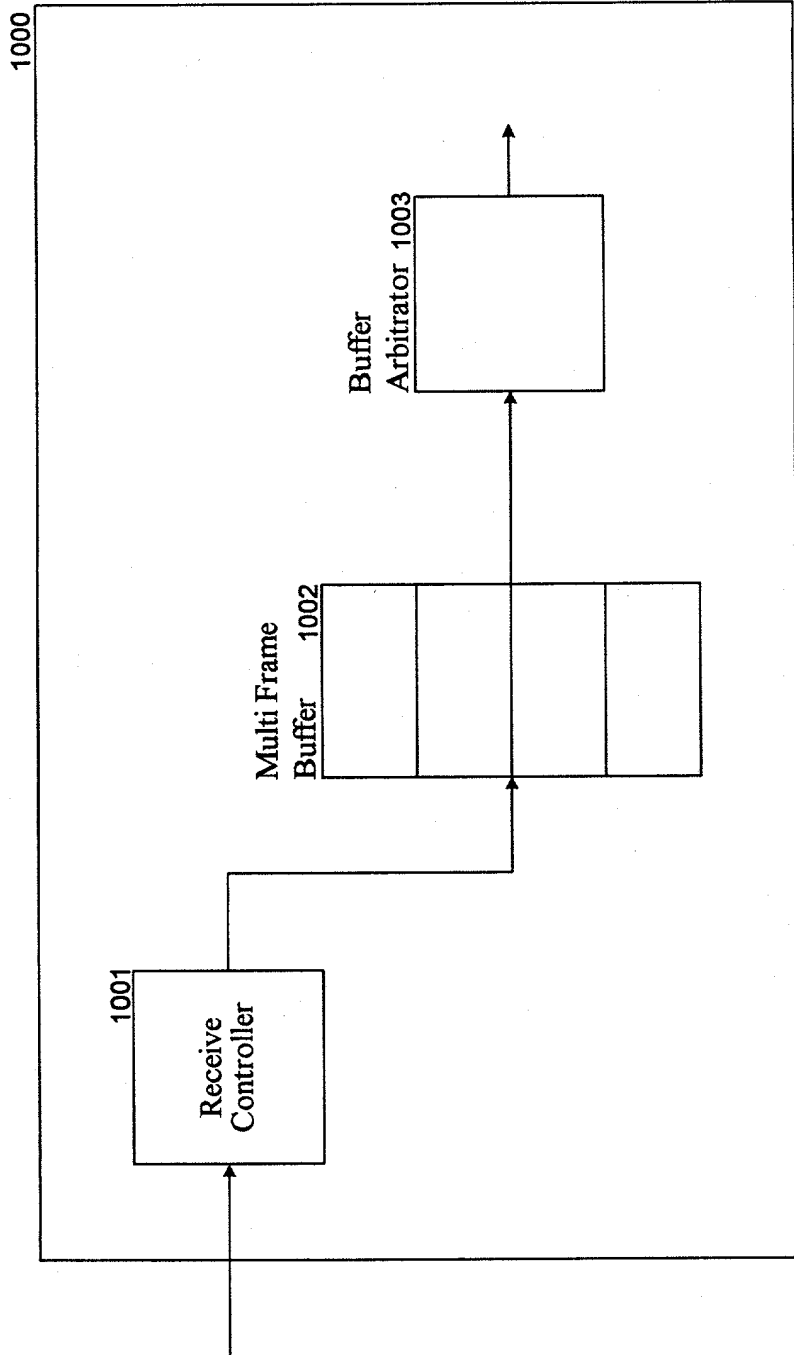


Fig. 10

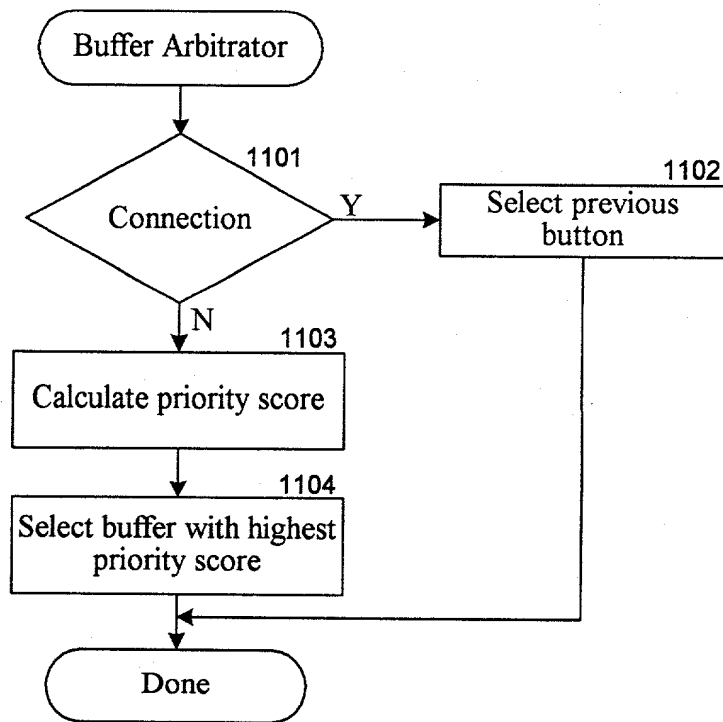


Fig. 11

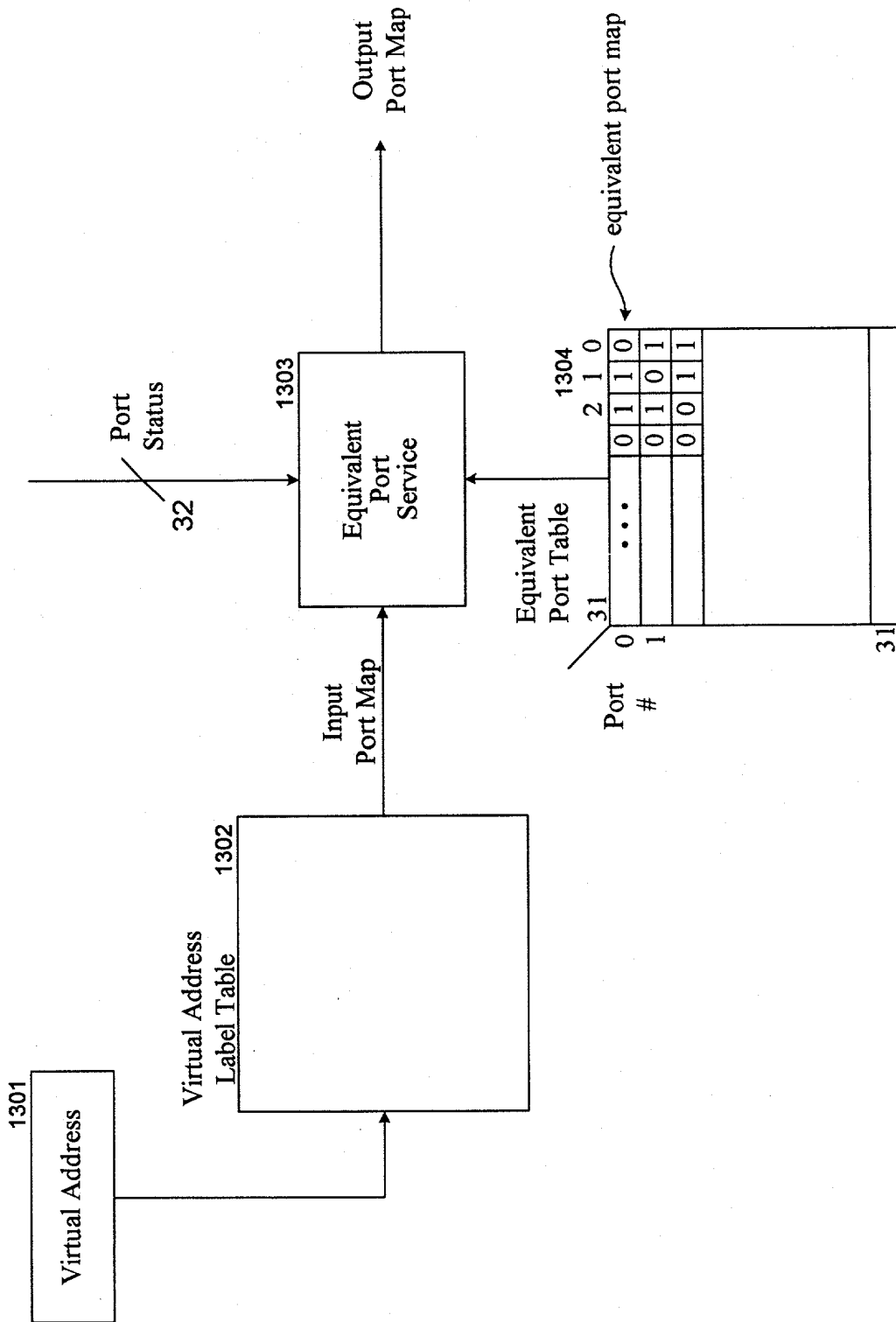


Fig. 13

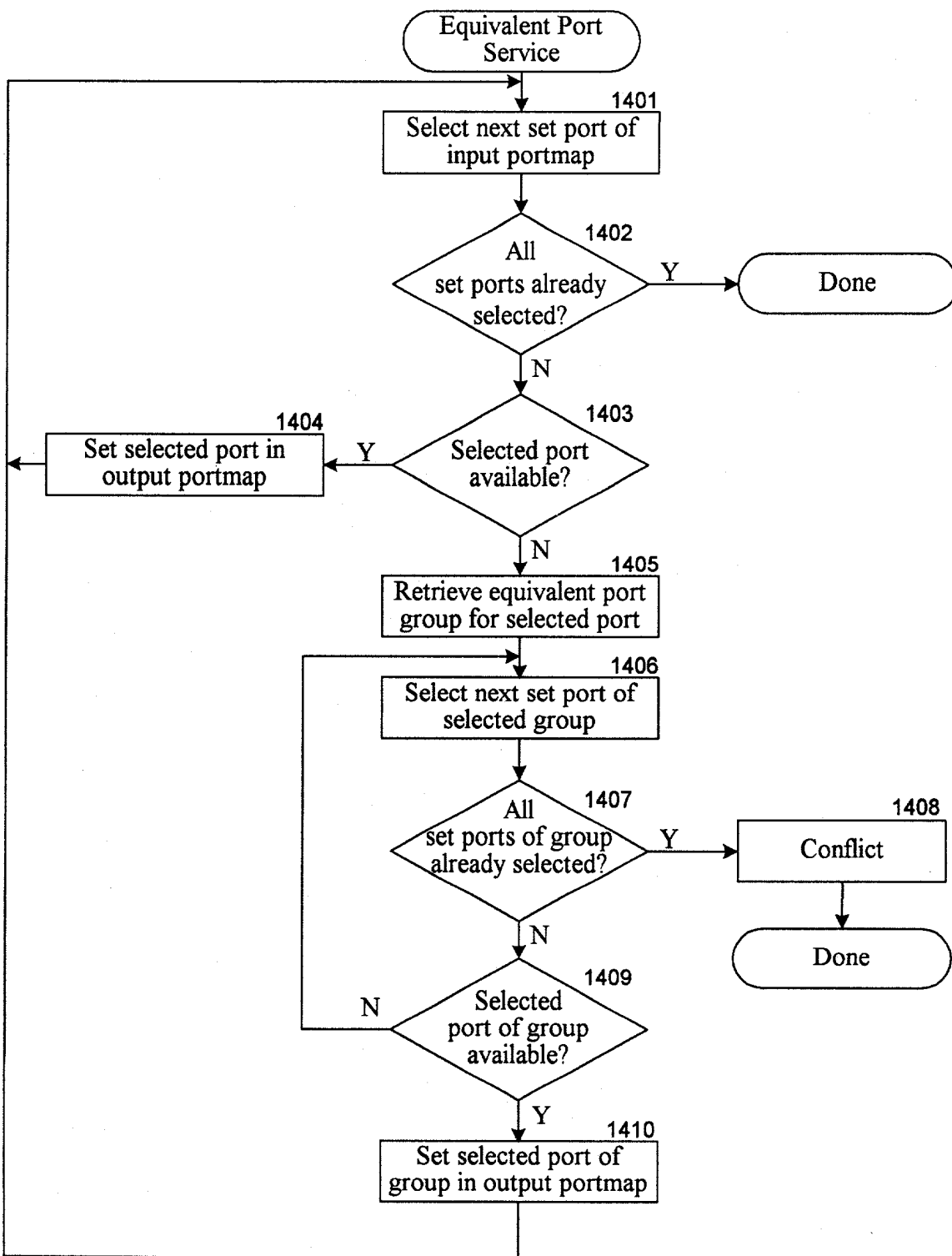


Fig. 14

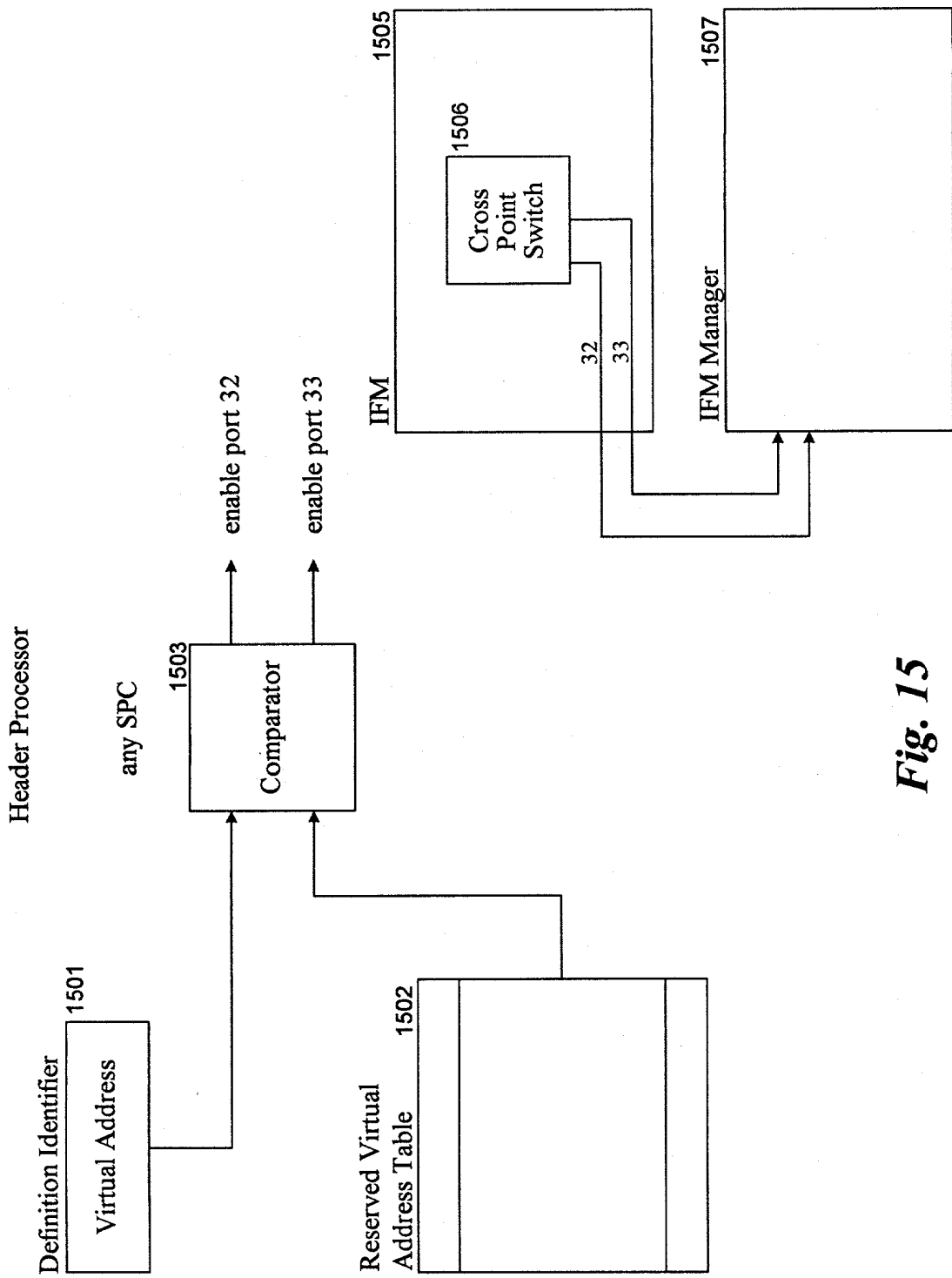
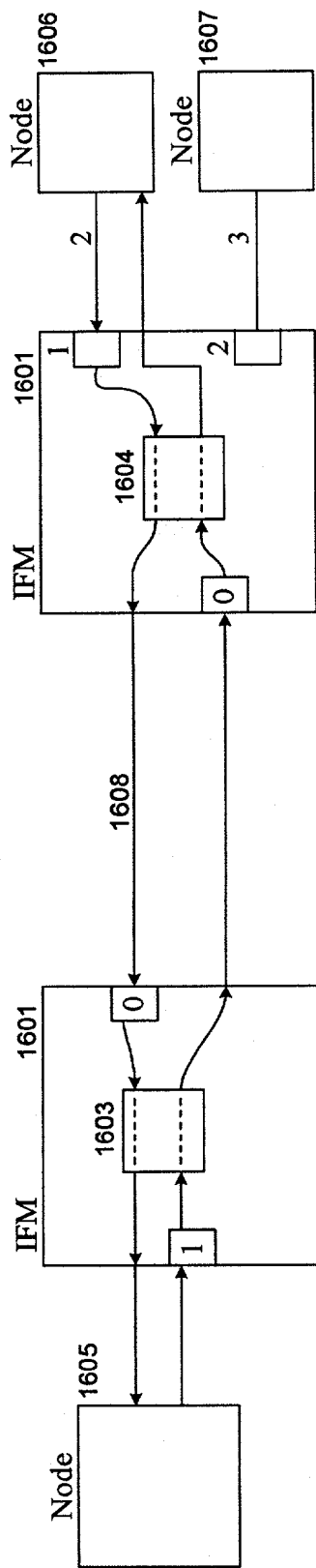


Fig. 15

FIG. 16 is a diagram illustrating a sequence of events in a system involving three nodes (Node 1605, Node 1606, Node 1607) and two IFM (Intermediate Forwarding Module) components (IFM 1601, IFM 1602). The diagram shows the flow of data and control signals between these components over time.



Deadlock 1610

| Time | Node 1605 | IFM 1601 | Node 1606 | IFM 1602 |
|------|--------------------|------------------------------------|--------------------|-----------------------------|
| 0 | Send start connect | | Send start connect | |
| 1 | | Connect 1↔0 | | Connect 2↔0 |
| 2 | | Forward start connect | | Forward start connect |
| 3 | | Can't forward start connect Node 1 | | Can't forward start connect |

Fig. 16

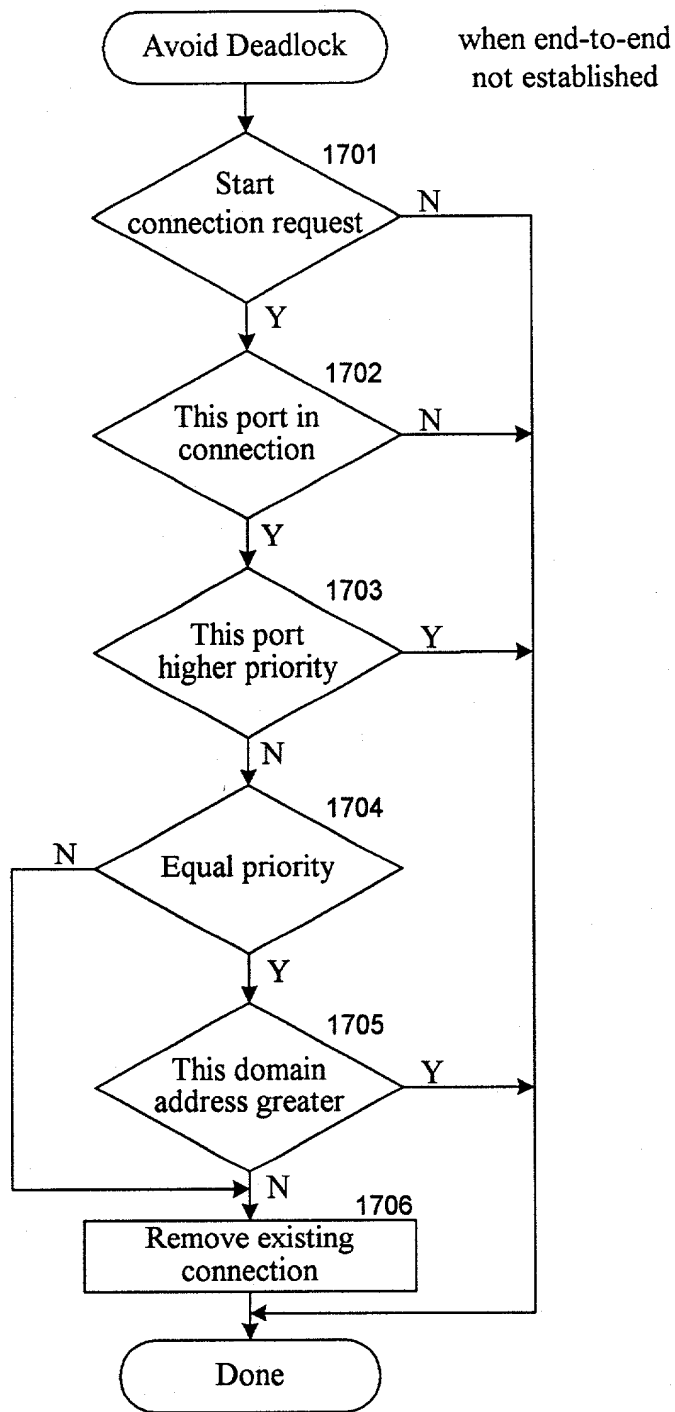


Fig. 17

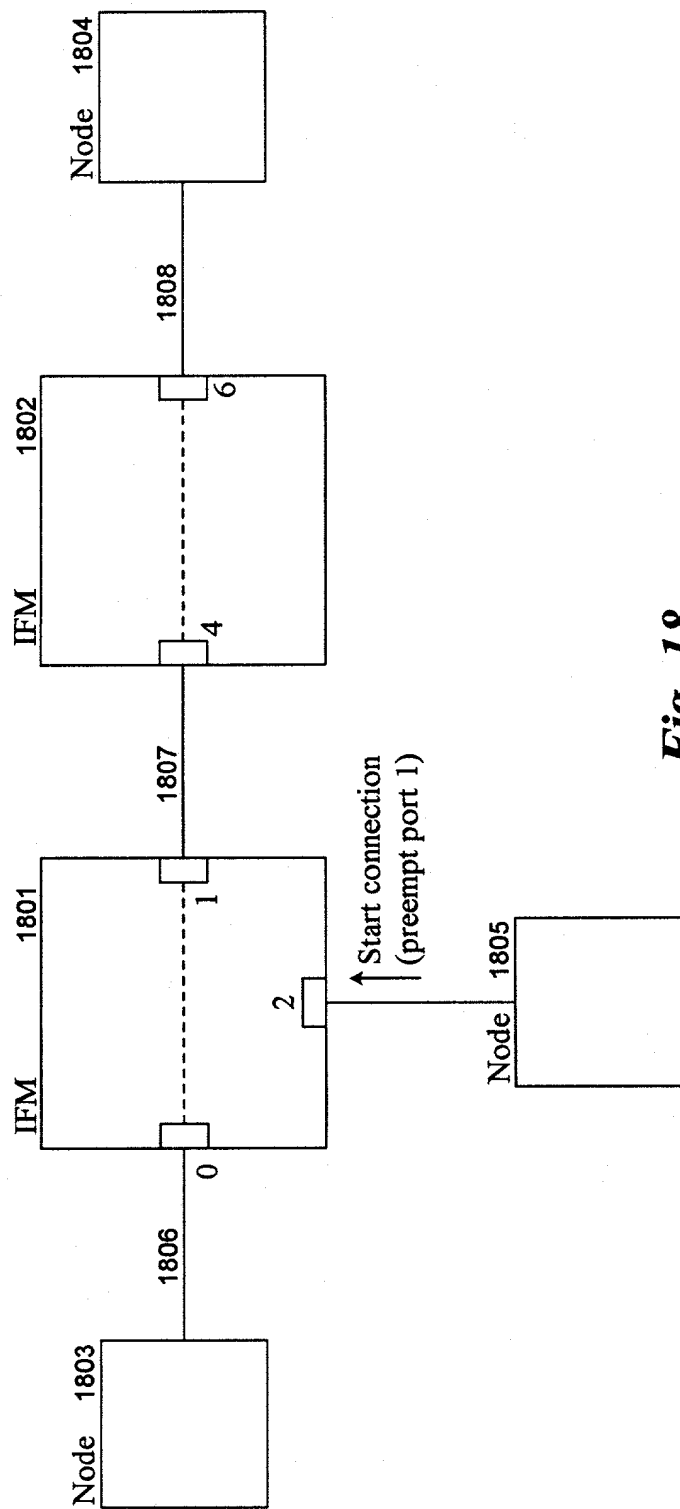


Fig. 18

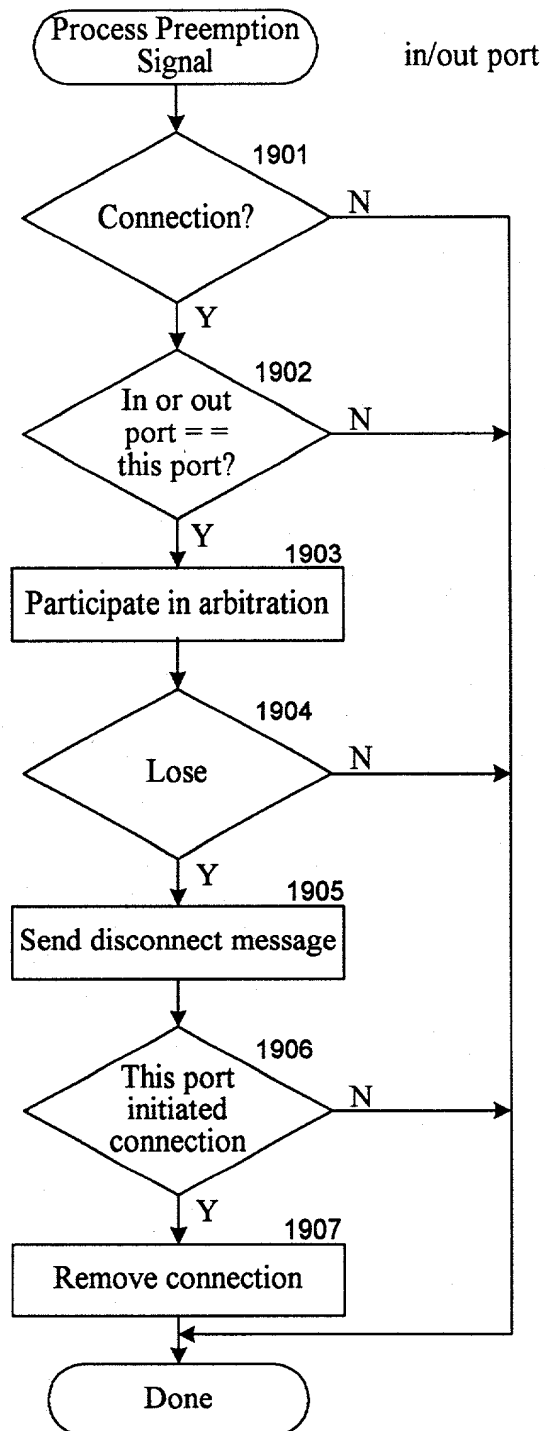


Fig. 19

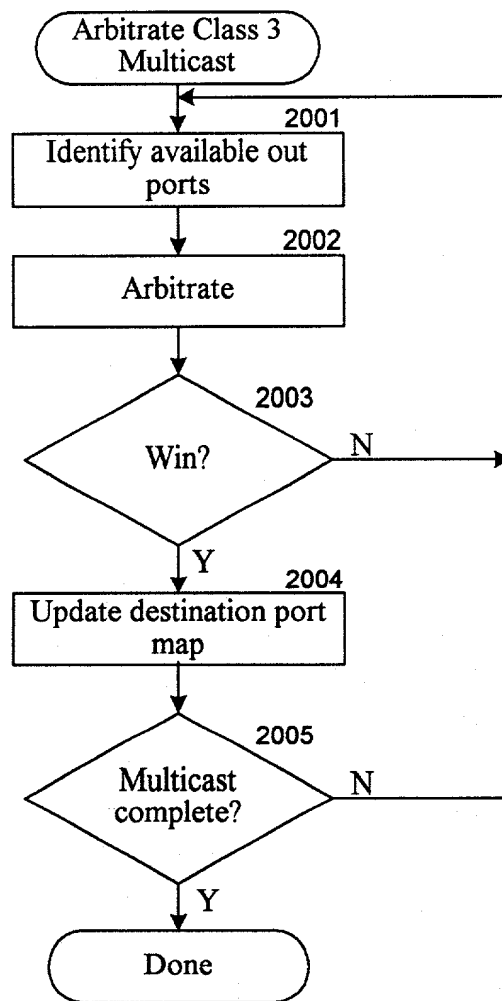


Fig. 20